

## Beware of FPGA

VIT University celebrating its silver jubilee in 2009 is a premier institution engaged in imparting quality technological education and providing support to research and development activities. VIT University, profoundly known as VIT was started in 1984 by Dr. G. Viswanathan. He is the founder and chancellor of the University and a former parliamentarian. The sole aim of the University is to provide quality education on par with International Universities at an affordable cost. Mr. Sekar Viswanathan and G. V. Selvam are serving the Universities as Pro-Chancellors. Dr. D. P. Kothari, the Vice Chancellor and Dr. Anand A. Samuel the Pro - Vice Chancellor are spearheading the Academic activities in VIT.

VIT is the first educational institute in India to get international accreditation (IET, United Kingdom) for its programmes. The University offers 15 undergraduate and 30 post graduate programmes. VIT is located in India's southern state of Tamil Nadu, between Chennai and Bengaluru [Bangalore]. Katpadi Railway station, that serves Vellore, is 3 km from VIT and is easily accessible by train and road from Chennai and Bengaluru [Bangalore].



**A planet to learn...**

**practice to grow ...**



*"When India leads the world  
VIT will lead India"*

**– Dr. G. Viswanathan, Chancellor**

History of computing discipline at VIT dates back to 1993, when the Department of Computer Science and Engineering was established to meet the demand for well-qualified computer professionals. The School of Computing Sciences (SCS) was conceived in the year 2004-05 and came into existence by consolidating achievements and core strengths of the aforesaid departments. At present, the School headed by Dr. M. Khalid has more than 5300 students and 226 core faculty members apart from many visiting professors, distinguished professionals from industry, eminent researchers. The school has one of the best infrastructure including domain-specific labs like Mobile Computing Research Lab, Intel Multi Core Research Lab, Motorola RFID Research Lab etc.

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**Nios® II**  
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**About Enixs Technology India Pvt. Ltd**

Enixs Technology India Pvt Ltd is one of the Fastest Growing Advanced Electronic Equipment Suppliers in India. Enixs is engaged in the Development of New Electronic Products and Manufacturing Processes and has Operational Centre at Chennai, Bangalore, Hyderabad, Pune and New Delhi. Enixs is well-known for its High Standard and Quality.

Enixs is Functioning with the Motto of "Tech Hope for Third World Countries". As a Credit to its Performance, Enixs Quickly became One of the Most Popular and Well – Respected Supplier of Embedded, VLSI and Test & Measurement Equipment in Major parts of India and Very Significantly it has become the University Programme Channel Partner for ALTERA Semiconductors USA, Promising World Leader of FPGA Manufacturers. Currently Enixs is functioning with an Excellent R&D Infrastructure Facilities and Technically Skilled Team of Engineers.

**Motivation behind the workshop**

VLSI Applications form the key to many emerging systems of modern technology. India has a rich manpower in Microelectronics domain. With the advent of FPGAs, a new era has emerged in the form of reconfigurable computing. FPGA brings a new paradigm of computing in terms of programmable processing.

Through this workshop we try to provide interesting insights into the world of FPGAs. The workshop will open mind gates to learn ALTERA EDA Tools. Lab exercises will provide the participants with hands-on experience under the guidance of expert instructors from ALTERA and ENIXS.





Name: .....

Designation: .....

Qualification: .....

Organization: .....

Address for correspondence:

Phone: .....

Email: .....

One Day Professor's Workshop  
on  
FPGA SYSTEM DESIGN USING  
ALTERA TOOLS

September 05, 2009.

**Organized by**



**School of Computing  
Sciences  
VIT University  
Vellore – 632014  
[www.vit.ac.in](http://www.vit.ac.in)**

Signature of the candidate

## WORKSHOP SCHEDULE

- 🕒 9.30 am Inauguration
- 🕒 10.00 am Introduction to ASIC & FPGA
- 🕒 11.00 am Tea Break
- 🕒 11.15 am Case study on FPGA Application
- 🕒 12.00 pm Introduction to Altera Tools
- 🕒 12.30 pm Lunch
- 🕒 01.30 pm Hands-on Implementation using Quartus II Software
- 🕒 02.30 pm Tea Break
- 🕒 02.45 pm Hands-on Implementation using SOPC Builder & NIOS II Processor
- 🕒 05.00 pm Valedictory Function

*Lucky Draw at the end of the Workshop*

**Prizes:** DE0 Kit from Altera, VLSI books

## WHO SHOULD ATTEND

The workshop is exclusively meant for teaching faculty in the engineering colleges. No TA/DA will be paid for the participants.

## RESOURCE PERSONS

Experts from ALTERA and Enixs Technology India Pvt Ltd.

## REGISTRATION FEE

Academicians : **Rs 500**

The applicant may mail the filled up registration form and the Demand Draft drawn in favour of “**VIT University**” payable at **Vellore**. No TA & DA will be provided.

The duly sponsored application forms are to be sent to:

**S. Sivanesan,**

Assistant Professor (Senior)  
SCS, VIT University, Vellore-632014,

Tamil Nadu, India.

Phone: 9600822649

Email: [ssivanesan@vit.ac.in](mailto:ssivanesan@vit.ac.in)

**One Day Professor's  
Workshop**

**on**

**FPGA System Design using  
ALTERA Tools**

**5 Sep. '09 (Saturday)**

**Sponsored by**

**ALTERA**

**Enixs**  
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