

**THE HOST INSTITUTION**  
**Netaji Subhas Institute of Technology**

Netaji Subhas Institute of Technology (NSIT) was established by the name of **Delhi Institute of Technology (DIT)** in July **1983 (at Kashmere Gate)**. The campus was shifted to present location in 1998. Started with a view to churn out highly competent, intelligent and dedicated engineers, the institute has more than achieved its motto by emerging as one of the leading tech schools of the country. Situated in the rapidly developing sub city of **Dwarka**, Delhi, NSIT is spread over a sprawling **145 acre lush green campus**.

**About Enixs Technology India Pvt Ltd**

Enixs Technology India Pvt Ltd, is one of the Fastest Growing Advanced Electronic Equipment Suppliers in India. Enixs is engaged in the Development of New Electronic Products and Manufacturing Processes and has Operational Centres at Chennai, Bangalore, Hyderabad, Pune and New Delhi. Enixs is an Impressive Firm, Renowned for its High Standard and Quality Business. Enixs is Functioning with the Motto of "Tech Hope for Third World Countries" and with the Vision of Technology Establishment throughout the Globe. Enixs is one of the Most Popular and Well – Respected Supplier of Embedded, VLSI and Test & Measurement Equipment in Major parts of India and Very Significantly it has become the University Programme Channel Partner for ALTERA Semiconductors USA, Promising World Leader of FPGA Manufacturers.

**THE HOST DEPARTMENT**  
**Division of computer engineering**

Since 1984, Computer Engineering division at NSIT is committed to provide a supportive, friendly and challenging environment for teaching, learning and research. Throughout our Bachelors, Masters and Ph. D. programs, our education model focuses on providing breadth and depth of knowledge in a multitude of computing disciplines like computer architecture, databases, networks, embedded systems, real-time systems, software engineering etc.

**Motivation behind the workshop**

VLSI Applications form the key to many emerging systems of modern technology. India has a rich source of manpower in Electronics in general and Microelectronics in particular. With the advent of FPGAs we have entered the era of reconfigurable computing. FPGAs act as a sort of system-level silicon super-glue, sitting at the periphery of the circuit board making anything to connect to anything else, so that we could adapt on the fly and change our design to leverage any new, improved component without having to abandon the rest of the design. Through this workshop we try to provide interesting insights into the world of FPGAs. The workshop will provide an understanding of Design-implementation flow involved in FPGA Design and Implementation using ALTERA EDA Tools. Lab exercises will provide the participants with hands-on tool usage experience under the guidance of expert instructors from ALTERA and ENIXS.

**REGISTRATION FORM**

**ONE DAY FACULTY WORKSHOP ON**  
**FPGA SYSTEM DESIGN USING ALTERA TOOLS**

**September 26, 2009**  
**(Saturday)**

Name : .....

.....

Designation : .....

Qualification : .....

Organization: .....

.....

Address for Correspondance: .....

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Phone : .....

E-mail : .....

Signature of the candidate

## Workshop Schedule

- 09.30 am Inauguration
- 10.00 am Introduction to ASIC & FPGA
- 11.00 am Tea Break
- 11.15 am Case study on FPGA Application
- 12.00 pm Introduction to Altera Tools
- 12.30 pm Lunch
- 01.30 pm Hands-on Implementation using Quartus II Software
- 02.30 pm Tea brake
- 02.45 pm Hands-on Implementation using SOPC Builder & NIOS II Processor
- 05.00 pm Valedictory Function

## Lucky Draw at the End of Workshop

**Prizes:** Altera Development Kit,  
ALTERA special edition books

## Who Should Attend?

The workshop is exclusively meant for teaching faculty in the engineering colleges. No TA/DA will be paid for the participants.

## Resource Persons

Experts from ALTERA and Enixs Technology India Pvt Ltd.

## Registration Fee

No registration fee will be charged.

The duly filled application forms are to be sent to:

**Co-ordinator:** Dr. M.P.S. Bhatia

Department of Computer Engineering

**Address:** Netaji Subhas Institute of Technology  
(Formerly, Delhi Institute of Technology)  
Sector-3, Dwarka New Delhi-110078

**Phone:** 09871479903 (Event manager)

Ph: 011-25099021

**Email:** [workshop@enixs.in](mailto:workshop@enixs.in)

## ONE DAY FACULTY WORKSHOP ON FPGA SYSTEM DESIGN USING ALTERA TOOLS

September 26, 2009  
(Saturday)



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Organized by



Division of Computer engineering  
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**New Delhi-110078**

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